



Robert McLauchlan

512-339-4100

p. 1

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34399 7590 04/20/2005

GARLICK HARRISON & MARKISON LLP
P.O. BOX 160727
AUSTIN, TX 78716-0727

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Robert A. McLAUCHLAN (Depositor's name)
[Signature] (Signature)
07/20/2005 (Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/721,203 | 11/25/2003 | Roy L. Vargas | SIG000119 | 5524 |

TITLE OF INVENTION: SYSTEM AND METHOD TO ESTABLISH AN ADJUSTABLE ON-CHIP IMPEDANCE

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|---------------------|--------------|----------------|-----------------|------------------|------------|
| nonprovisional | YES | \$700 | \$300 | \$1000 | 07/20/2005 |
| EXAMINER | ART UNIT | CLASS-SUBCLASS | | | |
| CHO, JAMES HYONCHOL | 2819 | 326-030000 | | | |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (prior or type)

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(A) NAME OF ASSIGNEE

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Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

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- ☒ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date

07/20/05

Typed or printed name

Robert McLAUCHLAN

Registration No.

44924

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